

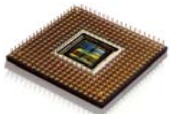
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# FPT'2010 in Beijing

Jinian Bian, General Chair,  
Kang Zhao, Secretariat

EDA Lab, Dept. Computer Science & Technology  
Tsinghua University, Beijing 100084, China

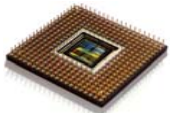
Oct. 24, 2009



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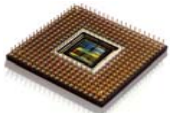
# Contents

- Conference Information
- Tsinghua Campus



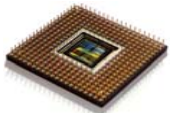
# FPGA Conferences

- 3 main conferences for FPGA
  - FPGA
    - International Symposium on Field-Programmable Gate Arrays
    - American Area, February
    - <http://www.isfpga.org>, <http://www.ece.wisc.edu/~kati/isfpga/>
  - FPL
    - International Conference on Field Programmable Logic and Applications
    - European Area, August
    - <http://www.icfpl.org>, <http://fpl2009.org/>
  - FPT
    - International Conference on Field-Programmable Technology
    - Asian and South Pacific Area, December
    - <http://www.icfpt.org/>



# Introduction of FPT

- FPT
  - The premier conference in the Asian region on FPGA
  - The 9th FPT will be held in Beijing in 2010
  - Previous FPTs
    - This year will be in Sydney (FPT'09)
    - Taipei (FPT'08), Kitakyushu (FPT'07), Thailand (FPT'06), Singapore (FPT'05), Brisbane(FPT'04), Tokyo(FPT'03), Hong Kong(FPT'02)
  - Instances (FPT'08 as example)
    - 135 submissions from 20 countries were received
    - 23% were accepted as regular papers, 25% were poster
    - About 150 attendees (50 local and 100 abroad)



# FPT'2010

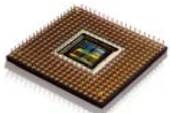
- Data and Venue

- Data: December 8-10, 2010
  - Not include tutorials and workshop
- Venues: Tsinghua University campus, Beijing, China
- Organizer: EDA lab, Dept. of Computer Science & Technology, Tsinghua University



- EDA Lab, Tsinghua

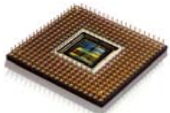
- Dept. of Computer Science & Technology, Tsinghua University
- Advisers: Prof. Xianlong Hong, Prof. Jinian Bian, Prof. Yici Cai
- Research: High-level synthesis, Physical design, FPGA tools, SoC, Verification, etc.
- <http://tiger.cs.tsinghua.edu.cn/>



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# Main Sessions (1)

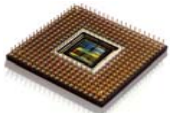
- **Tools and Design techniques for field-programmable technology**
  - placement, routing, synthesis, verification
  - debugging, run-time support, technology mapping
  - parallelization, timing optimization
  - provably-correct development



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# Main Sessions (2)

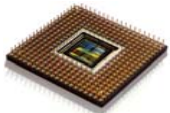
- **Architectures for field-programmable technology**
  - complex programmable logic devices
  - coarse-grained reconfigurable arrays
  - memory architectures, interface technologies
  - reconfigurable computing systems
  - high- performance reconfigurable systems



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# Main Sessions (3)

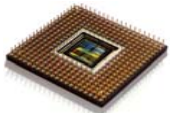
- **Device technology for field-programmable logic**
  - programmable memories such as non-volatile
  - dynamic and static memory cells and arrays
  - interconnect devices, circuits and switches
  - emerging VLSI device technologies



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# Main Sessions (4)

- **Applications of field-programmable technology**
  - biomedical and scientific computation accelerators
  - network processors, real-time systems
  - hardware emulation, digital signal processing
  - embedded applications, evolvable hardware

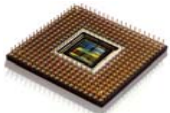


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# Main Sessions (5)

- **Education**

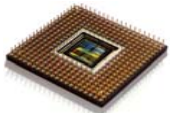
- Course, teaching and training experience,
- Experiment equipments, design and application
- Laboratory



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# Important Date

- Submission of regular papers 1 June 2010
- Notification of acceptance 10 August 2010
- Final Paper & Registration due 15 September 2010
- Design competition entries due 9 October 2010
- Conference starts 8 December 2010



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# Committees

- Steering Committee

Philip Leong (chair), University of Sydney, Australia  
(Former: CUHK, Hong Kong)

Neil Bergmann, University of Queensland, Australia

Masahiro Fujita, University of Tokyo, Japan

Wayne Luk, Imperial Collage London, UK

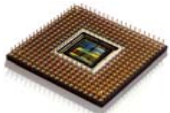
Wai-Kei Mak, Tsing Hua University, Hsingchu

Tadao Nakamura, Tohoku University, Japan

Theerayod Wiangtong, Mahanakorn University of Technology,  
Thailand

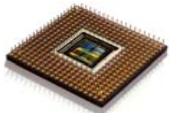
Steve Wilton University of British Colombia, Canada

Weng Fai Wong, National University of Singapore, Singapore

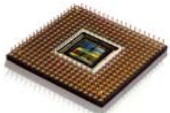
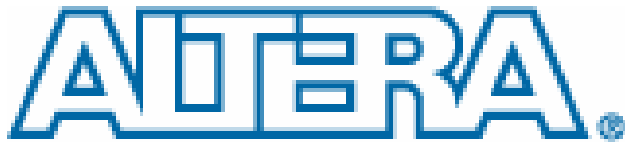
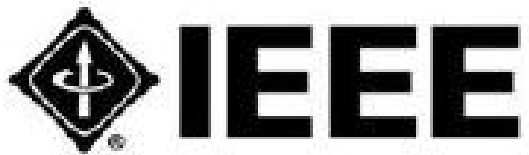


# Committees

- Local Organizing Committee
  - General Chair: Prof. Jinian Bian, Tsinghua University
  - Advisory chair: Prof. Xianlong Hong, Tsinghua University
  - Program co-chairs: Prof. Qiang Zhou, Tsinghua University  
Prof. Peter Athanas, Virginia Tech, USA  
Prof. Yajun Ha , NUS, Singapore
  - Local arrangement Chair: Prof. Yici Cai, Tsinghua University
  - Publicity co-chairs: Prof. Hideharu Amano, Keio University, Japan  
Prof. Jianwen Zhu, University of Toronto, Canada
  - Secretariat / Web chair: Dr. Kang Zhao, Tsinghua University
- URL
  - <http://www.icfpt.org>



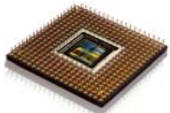
# Co-Sponsors (to be added)



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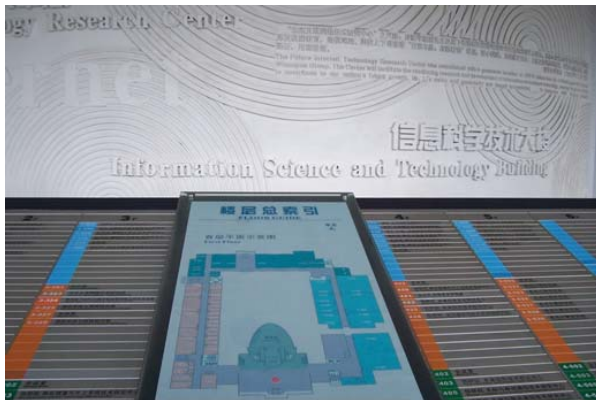


# Conference Venue

- FIT Building (bid)
  - Future Internet Technology (FIT)
  - School of Information Science and Technology
  - Inside the Ease Gate of Tsinghua



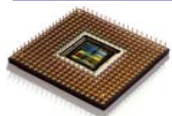
**FIT Building**



**FIT ichnography**



**FIT airscape**



# Assembly Room



**150-seat room**



**60-seat room**



**44-seat room**



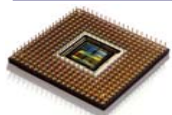
**35-seat room**



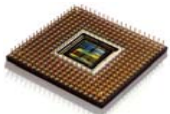
**20-seat room**



**Platform**



# Tsinghua University



# Delicious Food in Beijing



**Peking Roast Duck**



**Lv Da Gun**



**Mongolian Hot Pot**



**Naiyouzhagao**



**Chao Gan**



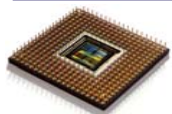
**Tang Er Duo**



**Mung Tofu**



**Candied & Preserved  
Fruit**



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# Welcome to FPT2010

# Thanks

