


Software

Arria® GX devices are supported by the Altera® Quartus® II design software, which provides a comprehensive environment for system-on-a-programmable-chip (SOPC) design. The Quartus II software includes HDL and schematic design entry, compilation and logic synthesis, full simulation and advanced timing analysis, SignalTap® II logic analyzer, and device configuration.

 For more information about the Quartus II software features, refer to the *Quartus II Development Software Handbook*.

The Quartus II software supports the Windows XP/2000/NT, Sun Solaris 8/9, Linux Red Hat v7.3, Linux Red Hat Enterprise 3, and HP-UX operating systems. It also supports seamless integration with industry-leading EDA tools through the NativeLink interface.

Device Pin-Outs

 Arria GX device pin-outs are available on the Altera web site at www.altera.com.

Ordering Information

Figure 5–1 describes the ordering codes for Arria GX devices.


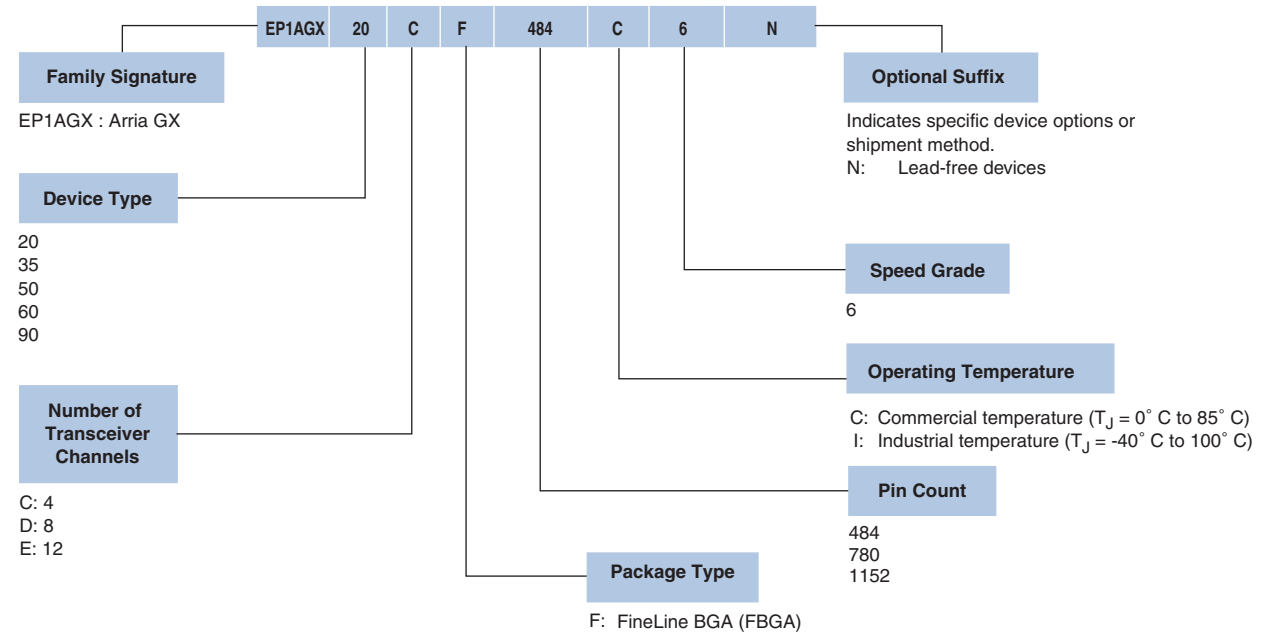
 For more information on a specific package, refer to the *Package Information for Arria GX Devices* chapter.

Figure 5-1. Arria GX Device Packaging Ordering Information

Document Revision History

Table 5-1 shows the revision history for this chapter.

Table 5-1. Document Revision History

Date and Document Version	Changes Made	Summary of Changes
December 2009, v2.0	<ul style="list-style-type: none"> ■ Document template update. ■ Minor text edits. 	—
August 2007, v1.1	Added the “Referenced Documents” section.	—
May 2007, v1.0	Initial Release.	—