



Altera's HardCopy devices enable ASIC-level performance and price without prohibitive ASIC development costs.

Altera's Silicon Solutions Program

Enabling Your Design Solutions

"We are facing an inflection point in our industry where chip development costs are rapidly increasing with each new process node. We believe that this is going to result in some fundamental changes in our industry."

– John Daane, President and CEO of Altera Corporation, Semiconductor Industry Association Annual Meeting

The Expense of Developing with Traditional ASIC Technology

According to industry experts, the total cost to develop a new application-specific semiconductor product (ASSP) using standard-cell ASIC technology can be as high as \$30M. This significant level of investment can not only introduce risk to a company, but can also impact the decisions that are made across its operations: from sales and marketing, to finance and engineering.

Mitigating Risk

Altera can help alleviate this investment risk. The Altera Silicon Solutions Program makes it easy for partners to prototype reference designs or companion devices using Altera FPGAs. Designing with FPGAs early in the development process gives partners the flexibility to adapt to changes in market requirements or allows them to explore additional markets with different silicon features.

The Race to Revenue

Partners can migrate their prototypes into volume production by using Altera's HardCopy™ structured ASICs. HardCopy devices enable them to get to market more quickly and with less development cost than a traditional ASIC. By operating outside the traditional ASSP deployment model, vendors shorten time-to-revenue, and ultimately time-to-profit.

Promote Your Intellectual Property

Altera's Quartus® II design software with SOPC Builder provides an easy-to-use development tools platform that supports the seamless integration of intellectual property (IP), including microprocessors and peripherals. Partners can also have their blocks represented in Altera's tool flow, complete with links to partner web sites embedded in our design environment desktop, reaching over 25,000 active designers.



Altera Offices

Corporate Headquarters
101 Innovation Drive
San Jose, CA 95134
USA
Telephone: (408) 544-7000
www.altera.com

Altera European Headquarters
Holmers Farm Way
High Wycombe
Buckinghamshire
HP12 4XF
United Kingdom
Telephone: (44) 1 494 602 000

Altera Japan Ltd.
Shinjuku i-Land Tower 32F
6-5-1, Nishi-Shinjuku
Shinjuku-ku, Tokyo 163-1332
Japan
Telephone: (81) 3 3340 9480
www.altera.co.jp

Altera International Ltd.
2102 Tower 6
The Gateway, Harbour City
9 Canton Road
Tsimshatsui Kowloon
Hong Kong
Telephone: (852) 2945 7000

Partner with the Leader in Innovation

Altera created the first erasable programmable logic device (PLD) in 1983, and has brought numerous technological advancements to the industry – including the first embedded processor FPGAs and the first FPGAs with high-speed serial interfaces. Our track record makes us the perfect partner to enable your market success.

About the Silicon Solutions Program

Key Goals

- Support ASSP vendors' product development and market validation process by providing access to Altera's silicon and software technology
- Conduct mutually beneficial marketing and sales programs that combine Altera's broad market strengths and the ASSP vendor's application-specific expertise

Partnership Benefits

Partnering with Altera brings the following immediate benefits:

- Discounted Quartus II design software subscriptions
- Free attendance to Altera customer training classes (available as space permits)
- Prioritized technical support via Altera customer applications engineers
- Discounted device pricing for prototyping
- Access to Altera's MegaCore® intellectual property functions

Additional Advantages

In addition, based upon mutual interest, Altera can work with our partners to explore the following areas:

- Low- to mid-volume IP delivery platforms using Altera programmable devices or Altera mask-programmed HardCopy devices
- IP or device model incorporation into Altera's system-level development tools
- Targeted sales and marketing programs