

**Quarterly Spotlight****[Download New Nios II and Quartus II v8.0 Software](#)**

Get the latest release of the Nios® II Embedded Design Suite (EDS) and Quartus® II software v8.0 today. This latest release includes several improvements including support for 40-nm Stratix® IV FPGAs and HardCopy® IV ASICs, an MMU and MPU for the Nios II processor, and Nios II Studio beta—a new software development environment for the Nios II processor that enables faster software compile times.

With this software release, you can, for the first time, [immediately design with the SLS Corp. USB 2.0 device controller core](#) in the SOPC Builder environment.

See what's new with the [Nios II EDS](#) and [Quartus II software](#), and [download](#) the new versions today.

Introducing  
**Nios II v8.0**  
and  
**Quartus II v8.0**

**Nios® II**



QUARTUS® II

**Embedded Software & Tools****[Get a uCLinux BSP for the Nios II Processor from SLS](#)**

SLS Corp. offers a free evaluation of their uCLinux BSP targeted for the Nios II Embedded Evaluation Kit. The uCLinux BSP will include SLS's Touch Panel LCD Controller IP, Ethernet 10/100 IP, PS/2 IP, SD card controller IP, and VGA IP (source or object). This package will get you started quickly on your uCLinux-based design. Download a free evaluation of this BSP and its related documentation and evaluation IP from SLS.

**[New Graphic Demos from Imagem Technologies for the Nios II Evaluation Kit](#)**

Download the latest demos for your Nios II Evaluation Kit that showcase the graphic performance of Imagem Technologies ATHLET IP. Tutorials, demos, and a free software evaluation kit are available from Imagem Technologies.

**Literature & Technical Resources****[Inspiration for Embedded Designs: Award-Winning White Papers](#)**

Check out the top 19 white papers of the latest Nios® II Embedded Processor Design Contest, with finalists selected from over 700 entries. See how you can use Nios II processors in applications such as motion control, networking, and signal, data, and image processing. Winning papers include:

- [Nios II Processor Based Camera Design](#)
- [Smart Self-Controlled Vehicle for Motion Image Tracking](#)
- [RTOS Acceleration Using Instruction Set Customization](#)
- [Intelligent Solar Tracking Control System Implemented in an FPGA](#)



- [Portable Telemedicine Monitoring Equipment](#)

### [Develop Embedded Systems with Altera FPGAs \(PDF\)](#)

From a "First-Time Designer's Guide" to "Verification and Board Bring-Up", the design guides that make up this handbook explain the best practices for developing embedded systems with Altera. If you're new to Altera's embedded products, the design guides will bring you up to speed quickly. Experienced users will learn how to improve efficiency and reduce design cycles. See how it can help you with your next design.

### ["Reduce Power by Adding Hardware Accelerators," Embedded.com](#)

The rule of thumb in embedded system design has been that adding hardware increases power demands. The careful use of hardware accelerators, however, inverts the rule: adding hardware can reduce power. Download the article and see for yourself.

### [App Note: Implementing an LCD Controller \(PDF\)](#)

This new application note shows how to create your own LCD module embedded design. The example used is based on the Nios II Embedded Evaluation Kit and includes background information on the LCD subsystem and porting guidelines. Download today.

### **Webcasts & Training**

#### [Training: New System-on-Chip Designer Curriculum](#)

Need to embed a soft-core processor in your FPGA? Learn how with the SoC Designer Curriculum. This course-by-course curriculum lists every class you need to become an expert SoC designer, including integrating processors and software on a Nios II Development Kit.

Altera also offers instructor-led training in locations around the US:

- [Designing with the Nios II Processor and SOPC Builder](#)
- [Developing Software for the Nios II Processor](#)

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