

Welcome to the November issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

Monthly Spotlight

[Altera's Cyclone FPGA Family Crosses 5 Million-Unit Mark](#)

Offering low cost and high performance, Cyclone™ devices have proven to be a compelling alternative to ASICs and ASSPs. Engineers designing for price-sensitive, high-volume applications, such as flat panel displays, telematics and home networking can now leverage FPGAs to improve time-to-market.

FPGAs, CPLDs & Structured ASICs

[Altera Extends Leadership in High Density 90-nm FPGAs](#)

With best-in-class manufacturing processes, Altera is the market share leader for 90-nm high-density FPGAs. The successful rollout of Stratix® II devices continues with the EP2S15, EP2S30, EP2S60, EP2S90, and EP2S130 devices now open for orders. The EP2S60 ES devices have been shipping since June 2004.

Software, Intellectual Property & Development Kits

[Maximize Performance With Quartus II Integrated Physical Synthesis](#)

Quartus® II software includes the only physical synthesis optimization technology available from an FPGA vendor. This technology is applied during the fitting stage of the compilation process and can be used with third-party synthesis tools or the Quartus II synthesis feature.

[Order the Nios II Development Kit, Stratix II Edition--Shipping Now](#)

This Nios® II development kit provides everything you need to create total system-on-a-programmable-chip solutions with Nios II embedded processors and Stratix II FPGAs. For a limited time, this board features an EP2S60 device (in place of the regular EP2S30 device) at no additional cost.

[Celoxica Announces DK Accelerator for SOPC Builder](#)

Celoxica's DK Accelerator design tool for Altera SOPC Builder creates new opportunities for the rapid creation of custom intellectual property (IP). DK Accelerator provides the design capability to add custom C-based components into Altera's SOPC Builder design flow.

[Shipping Now--Gidel PROC2S Development Board Featuring Stratix II](#)

The PROC2S board works as an ASIC prototype platform or as a stand-alone development board. The board features Stratix II EP2S60 or EP2S180 devices and a four-level memory structure including 32 Mbytes DDR II. Up to 254 user I/O pins are available via one SCSI-5 connector and four daughter card connectors.

Technology & System Solutions

[Visit Altera's New Military & Aerospace Web Site](#)

Explore Altera's latest resources and solutions for military and aerospace applications, including secure communication, radar and sonar, electronic warfare guidance, and control and COTS computer.

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Optimize your DSP video & image processing designs. Watch this three-part DSP net seminar series on video and image processing that features leading-edge product demonstrations and experts from Barco Silex, The MathWorks, and Altera.

[View Now: Accelerate System-Level Design Net Seminar](#) **Free**

This net seminar shows you how to use Altera® FPGAs and development tools to quickly and easily create custom companion chips for your processor.

[See Altera's ASI Solution at the Advanced Switching Seminar](#) **Free** Munich, Germany, November 8, 2004

Attend and learn how Altera's advanced switch interconnect (ASI) solution enables designers to integrate Modelware's ASI IP core into Stratix GX FPGAs to interface devices to ASI switch fabrics.

Recent Articles about Altera

[Integrating High Speed Serial Transceivers into an FPGA](#), EE Times

Learn about the challenges faced by chip designers when integrating high-speed transceivers into FPGAs. Issues and solutions are discussed for floor planning, design methodologies, layout, and packaging.

[Metal Mangling Mayhem](#), FPGA Journal

Read how the CycloneBot, a combat robot that uses a Cyclone FPGA and Nios processor for its central nervous system, performs in the 2004 Robot Fighting League (RFL) National Championships.

[Jump on the PCI Express Bus Using FPGAs](#), Network Systems Design Newsletter

Find out why FPGAs provide the ideal solution for PCI Express applications. FPGAs offer a way for designers to quickly and inexpensively implement PCI Express without the long time periods usually required when adopting new protocols.

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