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InsideEdge

Altera's Monthly e-Newsletter

Welcome to the August issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

August 2005

Monthly Spotlight

[Join The MathWorks, Barco & Altera at the DSP Net Seminar Series](#)

Learn how the next-generation of FPGA solutions will help you develop architectures that boost digital signal processing (DSP) performance and lower overall costs. Hear from the experts at this three-part Code:DSP Video, Image and Signal Processing net seminar series. Register Today! **Free**

FPGAs, CPLDs & Structured ASICs

[Meet Your Power Budget Without Surprises Using Stratix II FPGAs](#)

Board respins, changes to power circuitry, changes to cooling solutions, & degraded reliability are all costly effects of bad power estimation data. See how Stratix® II and the accuracy of PowerPlay allow you to meet your power budget without surprises, unlike competing FPGAs.

Software, Intellectual Property & Development Kits

[Get Technical Details About the New Quartus II Version 5.0 Features](#)

The Quartus® II Version 5.0 Handbook guides you through the programmable logic design cycle from design to verification. Review newly published technical content on features like Incremental Compile, I/O Planning and Power Reduction Techniques.

Technology & End Markets

[Discover How Customers Are Using Programmable Logic](#)

Developers are leveraging the power of programmable logic in a variety of innovative and exciting products ranging from digital TVs to wireless network security. Visit Altera's Customer Showcase to explore over 75 products powered by Altera® solutions.

[Download Reference Design: Automotive Graphics Controller](#) **Free**

The automotive graphics controller design shows the power and flexibility FPGAs offer in low-cost applications. A video module provides clipping, color space conversion, and scaling. The design also features a graphics library running on a Nios® II processor.

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Events, Training & Net Seminars

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[Register for "Comparing Stratix II & Virtex-4 Power" Net Seminar](#) **Free**

September 8, Online - Find out which design types yield more accurate power estimates -- Altera's Quartus II PowerPlay vs. Xilinx's XPower. Learn how to compare Stratix II & Virtex-4 power and the technical details about the benchmark methodology. Enter to win a RCA Lyra Audio/Video Jukebox.

[Attend "Designing With High-Speed Serial Interconnects" Seminar](#) **Free**

September 22, Worcester, MA - Join us to learn valuable principles, design rules, tools and technology options for designing high-speed serial link interconnects at speeds up to 6.375 Gbps. Key features of Altera's upcoming transceiver product, Stratix II GX, will also be discussed.

[See Demonstrations of Altera's Video Solutions at IBC 2005](#)

September 9 - 13, Amsterdam - Visit Altera and its partners at the International Broadcasting Conference (IBC), to see demonstrations of integrated, cost-effective, performance-intensive video solutions implemented with Altera FPGAs.

[See Altera's Low-Cost PCI Express Solution at the TI Booth at IDF](#)

August 23 - 25, San Francisco, CA - The leading low-cost FPGA architecture and Texas Instrument's third generation transceiver technology provide a low-cost PCI Express Solution. To see this low-risk, highly-flexible, low-cost PCI Express solution, visit TI booth #914 at the Intel Developer Forum.

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Support & Literature

[Use the High-Density FPGA Matrix to Compare Competing FPGAs](#)

The High-Density FPGA Matrix helps you accurately compare the densities of competing FPGAs. It includes the Stratix II vs. Virtex-4 logic density comparison chart, Quartus II area optimization settings, density benchmark results, and FPGA architecture references.

[Download the Updated Cyclone II Handbook](#)

The Cyclone™ II handbook has recently been updated to reflect new lower cost and smaller form factor packages, updated preliminary timing models for all family members and enhanced I/O standard support. Download your updated copy today.

[New Tools to Design & Debug Configuration & Programming Schemes](#)

The new Configuration and Programming Centers on Altera.com are loaded with resources to help you design and debug configuration and programming schemes. You'll find programming tools and a useful configuration schemes matrix that allows you to view any mode with a single click.

Altera in the News

[Serious Attention Focused on 90-nm HardCopy Structured ASICs](#)

Michael Santarini of EDN discusses the value of structured ASICs at both 90-nm and 65-nm for mid-volume, mid-priced applications. Altera is now ramping



up capacity for their HardCopy® II structured ASICs as demand increases for guaranteed migrations from Stratix II FPGA designs.

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