

November 2006 *Inside Edge*

Bringing you the latest Altera product and solution news.

---

## Monthly Spotlight

---

### [Introducing Stratix III—The Lowest Power High-Performance FPGAs](#)

With 25% higher performance, 50% lower power and 2x the density of Stratix® II FPGAs, Stratix III is the latest high-end FPGA family from Altera. Combined with Quartus® II software, Stratix III FPGAs provide the most productive high-end FPGA solution available anywhere.

---

## FPGAs, CPLDs & Structured ASICs

---

### [Stratix II GX: View a Thousand Eye Diagrams with Eye Viewer](#)

Embedded transceivers can make or break a design. Only transceivers that have been designed robustly and characterized extensively yield successful results. Don't settle for a few eye diagrams when you can have a thousand eyes with Stratix II GX FPGAs.

[Back to top ▲](#)

---

## Software, Intellectual Property & Development Kits

---

### [Quartus II Version 6.1—Find Out What's New!](#)

Quartus II software v6.1 brings you enhanced PowerPlay technology along with expanded TimeQuest timing analyzer capabilities. Learn more about what's new in v6.1 and how these features help you get products to market faster.

### [Quartus II Software Is #1 in Productivity for 65nm FPGAs](#)

Introduced today, Quartus II software v6.1 offers industry-leading productivity advantages by delivering 50% faster compile times for the new Stratix III FPGA family compared to competing 65nm FPGA devices.

[Back to top ▲](#)

---

## Technology & End Markets

---

### [SI Prescription with Dr. Bogatin: Need High-Speed Network Testing?](#)

Have a need for speed? This month's SI Prescription from Dr. Bogatin is a fast-moving and highly informative article about the Interoperability Lab at the University of New Hampshire, the high-speed networking industry's largest, most extensive independent testing lab. Don't miss it.

### [Reference Design: OFDMA Ranging Design for WiMAX \(PDF\)](#)

Discover how Altera's devices, tools, and IP can be used to efficiently

## In This Issue:

[FPGAs, CPLDs & Structured ASICs](#)

[Software, Intellectual Property & Development Kits](#)

[Technology & End Markets](#)

[Events, Training & Net Seminars](#)

[Support & Literature](#)

[Altera in the News](#)

## On the Edge



[Click to enlarge](#)

See the world's first-ever comic devoted to the programmable logic industry.

---

## Did You Know...

You can get hi-tech from lentils, the vegetable? [Here's how...](#)

---

[Subscribe to Inside Edge](#)

implement OFDMA ranging functionality for mobile WiMAX base stations. This reference design can be used to accelerate designs based on the 802.16e-2005 standard.

### [HardCopy Advantages Become Crystal Clear in HDTV Security Cameras](#)

Find out why the world's leading provider of high-definition (HD) video surveillance solutions chose HardCopy® structured ASICs and Stratix FPGAs for delivering maximum digital signal processing (DSP) performance in their award-winning cameras.

[Back to top ▲](#)

---

## Events, Training & Net Seminars

---

### [Attend Altera's High-Speed Serial I/O Workshops](#) **Free**

Nov 2006 - Feb 2007, worldwide locations - Altera and its partners will host high-speed serial I/O workshops featuring complete protocol solutions, best board design practices and signal integrity optimization tutorials. Attend these workshops and gain experience with Altera's Stratix II GX serial I/O solutions.

### [Attend the New High-Speed Design Class Using Stratix II GX Devices!](#)

Instructors will demonstrate how to implement high-speed I/O protocols using Stratix II GX multi-gigabit transceivers, and address board design and signal integrity issues. And, you'll be using the Stratix II GX signal integrity board for hands-on lab exercises.

[Back to top ▲](#)

---

## Support & Literature

---

### [New! Sign Up for Altera's Technical Literature Update Notifications](#)

Sign up today to be notified when literature has been added or updated on the Altera Website. You will receive notifications about new and updated datasheets, app notes, white papers, errata and more.

### [Megafunction User Guides Updated with New Design Examples](#)

Get hands-on experience by downloading design examples supplied with the user guide. Quartus II project files and simulation files are included.

### [App Note: Accelerate a Scatter-Gather DMA with Checksum Using C2H](#)

Download this application note (PDF) and you'll discover how to use the C2H compiler to convert C code for a scatter-gather DMA function into a hardware accelerator.

[Back to top ▲](#)

---

## Altera in the News

---

### [Taking Programmable Logic to the Battlefield: A Strategy for Military IC Suppliers, MIL/COTS Digest](#)

This timely article covers how soldiers are using the full arsenal of Altera FPGAs from Stratix high-performance, high-density devices in radar to low-power, low-cost Cyclone® devices in radios. Because they stake their lives on



this equipment, FPGAs must meet or exceed special standards.

## [PLD-based video interfaces connect consumer electronics](#), **Electronic Products**

Portable media players, mobile phones, video games — all require interfaces and adapters to transfer video data to/from computers and other “gadgets.”

Find out about Altera’s I<sup>2</sup>C (available in VHDL and optimized for Altera’s PDL families) and about downloadable reference designs.

[Back to top ▲](#)

---

[Subscribe](#) to additional Altera e-mail updates and e-Newsletters, or view/edit all of your Altera e-mail subscriptions. Other Altera email communications include:

- Product Announcements & Updates
- Embedded e-Newsletter
- Code:DSP e-Newsletter
- Net Seminar e-Newsletter

Copyright© 1995-2006 Altera Corporation, 101 Innovation Drive, San Jose, California 95134, USA