

Welcome to the April issue of Altera's monthly Net Seminar e-Newsletter. Each issue gives you information on upcoming & recently broadcasted net seminars.

April 2005

Free Upcoming Net Seminars & Net Seminar Series



[EE Times Industry Challenge Net Seminar: Choosing the Right Interconnects](#)

Date: Thursday, April 21, 2005
Time: 11:00 AM Pacific Time **Free**

At a time when a variety of different interconnect options are becoming available--including Advanced Switching, Backplane Ethernet, HyperTransport, Infiniband, PCI Express, and RapidIO™--this net seminar will discuss which chip-to-chip and backplane interconnects designers are using today, expect to be using tomorrow, and why they are choosing them. The panel will also explore trends in the use and integration of high-speed SERDES, signal conditioning methods, and simulation models.

Recently Broadcasted Net Seminars & Net Seminar Series



[Accelerating Embedded System Designs With Soft-Core CPUs in FPGAs](#)

Available Now! **Free**

This net seminar describes how using FPGAs with soft-core processors, such as Altera's Nios® II embedded processors, can accelerate your system designs. In addition, the [Nios II development kit](#) is featured in an online demonstration to show you how quick and easy it is to implement multiple processors on a single chip.

[Try the Nios II Development Kit Online & Win One of Five Nios II Evaluation Kits, Stratix® II Edition \(US\\$295 Value Each\)](#)

What Attendees Are Saying:

This [FPGA Video Displays](#) net seminar not only provided a good summary of the different digital display technologies and the functional blocks, but also showed specifically where Altera's FPGA solutions could be implemented.

Wade Ichishita
Engineer,
Sony Electronics



[View Now](#)

This net seminar shows you how to mitigate FPGA power with appropriate thermal design, estimate power with tools, and compare various FPGAs with regards to power solution.

[View Now](#)

Available Now! **Free**

This net seminar focuses on the latest digital television video display technologies and discusses the benefits and features of using Altera's new Cyclone™ II FPGA family when designing flat panel displays.

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Congratulations



Chris Reimer (left), a project manager for Hewlett-Packard, recently won an Apple 20GB iPod by viewing the [HardCopy® II Structured ASIC](#) net seminar live. Congratulations, Chris!

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