

Welcome to the August issue of Altera's monthly Net Seminar e-Newsletter. Each issue gives you information on upcoming & recently broadcasted net seminars.

Aug 2005

Free Upcoming Net Seminars & Net Seminar Series

Code: DSP
Register Now

[Code: DSP - Video, Image & Signal Processing Net Seminar Series](#)
3 sessions from The MathWorks, Barco, & Altera.
Register Now! **Free**

Altera will host a three-part net seminar series on video, image and signal processing featuring experts from The MathWorks, Barco Silex, and Altera. Learn about

- Architecting systems with DSP processors & FPGA co-processors
- DSP design flow using Matlab/Simulink and DSP Builder/SOPC Builder
- Compression solutions: JPEG2000, MPEG4, H.264 & Windows Media 9

[Register for the Code: DSP Net Seminar Series Now!](#)

Upcoming Net Seminar Series Highlights

Part 1:	Video, Image & Signal Processing With FPGAs (Altera) Date: Wednesday, September 14, 2005 Time: 11:00 AM Pacific Time <i>Win a RCA Lyra Audio/Video 20 GB Jukebox (US \$400 value), built with Altera devices</i>
Part 2:	Low-Cost Solutions for Video Compression Systems (Barco Silex) Date: Wednesday, September 21, 2005 Time: 11:00 AM Pacific Time <i>Win a Pinnacle MovieBox Deluxe (US\$ 200 value), built with Altera devices</i>
Part 3:	Model-Based Design With Simulink (The MathWorks & Altera) Date: Wednesday, September 28, 2005 Time: 11:00 AM Pacific Time <i>Win a RCA Lyra Audio/Video 20 GB Jukebox (US \$400 value), built with Altera devices</i>

What Attendees Are Saying:

The [Addressing Base Station Design Challenges for 3GPP Release 6 & Beyond](#) net seminar informed me in greater detail about the industry spec, the challenges, and about how Altera can help me to meet my customer's needs in advancing to the next-generation products.

Frank Liao, Hardware Engineer, Motorola




[Attend all sessions to win a Sony DVD Recorder \(US\\$700 value\)](#)

Recently Broadcasted Net Seminars & Net Seminar Series



[View Now](#)

[Fit More Logic in Altera's Stratix II FPGAs Than in Xilinx's Virtex-4 FPGAs](#)

Available Now in English and Simplified Chinese!

[Free](#)

When is 180 > 200? When you compare Stratix® II devices to competing FPGAs. Find out why you can fit more logic in Altera's Stratix II devices than in competing FPGAs at this net seminar.



[View Now](#)

[Easily Implement PCI Express Into Your Design](#)

Available Now! [Free](#)

Find out how Altera's programmable, hardware-proven PCI Express solution can simplify your PCI Express design and minimize design risk.



[View Now](#)

[Addressing Base Station Design Challenges for 3GPP Release 6 & Beyond](#)

Available Now! [Free](#)

Find out about the trends and challenges involved in next generation cellular base station designs and how they can be addressed with Altera FPGAs.



[View Now](#)

[Understanding Jitter Theory, Measurement & Reduction Techniques](#)

Available Now! [Free](#)

Find out how to troubleshoot your systems to result in better system design by understanding basic jitter theory.



HANDHELD

View Now

[Building Handheld Systems in Minutes Using FPGAs](#)

Available Now! **Free**

Find out how to quickly and efficiently implement the key building blocks for handheld systems with Altera FPGAs.

[Back to top ▲](#)

Congratulations



John Li (right), a hardware engineer for KLA-Tencor, recently won a Gateway DVD Recorder/Player, built with Altera devices, by viewing the [Reduce System Cost With Low-Cost Programmable Logic in Common Applications](#) net seminar live. Congratulations, John!

[Back to top ▲](#)

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