

Welcome to the September issue of Altera's monthly Net Seminar e-Newsletter. Each issue gives you information on upcoming & recently broadcasted net seminars.

Free Upcoming Net Seminars & Net Seminar Series

Code: DSP [Register Now](#) [Code: DSP - Video, Image & Signal Processing Net Seminar Series](#)
 3 sessions from The MathWorks, Barco & Altera.
Register Now! [Free](#)

Altera will host a three-part net seminar series on video, image and signal processing featuring experts from Barco Silex, The MathWorks, and Altera.

Upcoming Net Seminar Series Highlights

Part 1:	Video, Image & Signal Processing With FPGAs (Altera) Date: Wednesday, September 14, 2005 Time: 11:00 AM Pacific Time
Part 2:	Low-Cost Solutions for Video Compression Systems (Barco Silex) Date: Wednesday, September 21, 2005 Time: 11:00 AM Pacific Time
Part 3:	Model-Based Design With Simulink (The MathWorks & Altera) Date: Wednesday, September 28, 2005 Time: 11:00 AM Pacific Time

[Attendees will be eligible to win prizes, including a Sony DVD Recorder \(US\\$700 value\)](#)

[Comparing Stratix II & Virtex-4 Power & Power Estimation Accuracy](#)

Date: Thursday, September 8, 2005 [Free](#)
 Time: 11:00 AM Pacific Time



What Attendees Are Saying:

The benchmarking comparison in the ["Fit More Logic in Stratix II Than Virtex-4"](#) net seminar is very realistic. As an active user of both Stratix-II and Virtex-4, I could easily understand the advantages of Altera devices discussed during the seminar. The knowledge gained through the seminar is useful in making device selections for a large density design with more combinational logic elements.

Awanish Verma,
 Hardware Engineer,
 Cisco Systems



Find out which design types yield more accurate power estimates -- Altera's Quartus II PowerPlay vs. Xilinx's XPower. Learn how to compare Stratix II & Virtex-4 power and the technical details about the benchmark methodology.

[Win an RCA Lyra 20 Gbit Audio/Video Jukebox \(US\\$400 value\), built with Altera® devices.](#)



**10-Gbps
Traffic Manager**

[Register Now](#)

[Enforce Quality of Service With a Customizable 10-Gbps Traffic Manager](#)

Date: Tuesday, September 27, 2005 [Free](#)

Time: 11:00 AM Pacific Time



Find out how Altera's 10-Gbps Traffic Manager solutions can enable equipment manufacturers to customize their QoS requirements and retain the flexibility to meet the changing needs of the market.

[Win a Pinnacle MovieBox Deluxe \(US\\$200 Value\), Built With Altera Devices](#)

JITTER

[Register Now](#)

[Improve High-Speed System Debug by Understanding Jitter Equipment Principles and Measurement](#)

Date: Thursday, October 6, 2005 [Free](#)

Time: 11:00 AM Pacific Time

Learn about the latest industry perspective on Jitter/BER measurement equipment. Discover the principles behind jitter measurement equipment which will allow your customers to pick the appropriate equipment to perform high-speed system debug.

[Win a Gateway DVD Recorder \(US\\$250 Value\), Built With Altera Devices](#)

SerDes

[Register Now](#)

[Interfacing Multi-Serial Protocol Discrete SerDes to Increase FPGA Performance](#)

Date: Tuesday, October 11, 2005 [Free](#)

Time: 11:00 AM Pacific Time

In this seminar you will learn the advantages and trade-offs of using discreet PHYs and FPGAs, PMC-Sierra's discrete SerDes devices, Altera's 90nm FPGAs and Structured ASIC, various design examples, and Altera & PMC design support capabilities.

[Win a Gateway DVD Recorder \(US\\$250 Value\), Built With Altera Devices](#)

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Congratulations



Vitaliy Tkachenko, a manager for Xilinx Technologies, recently won a Pinnacle MovieBox Deluxe (US\$200 value), built with Altera devices, by viewing the ["Easily Implement PCI Express Into Your Design"](#) net seminar live.

Congratulations, Vitaliy!

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