

July 2007: Altera's monthly net seminar e-newsletter provides schedules and descriptions of upcoming and recently broadcast Altera® online seminars.



[Guidelines for Designing Gigabit Channels](#) **Free**

Duration: 40 minutes

View Now, On Demand!

Though high-speed serial links increase data throughput and reduce the number of traces on a board, new challenges arise when designing these systems. This seminar provides useful guidelines and techniques for designing a high-speed channel. The seminar covers a channel model case study that includes how to address the challenges of designing at high data rates (BGA breakout, crosstalk, vias, DC block capacitor, and an SMA connector). These challenges are tied in and are implemented in a complete end-to-end channel simulation.

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[Learn to Design with Stratix® III FPGAs' Programmable Power Technology and Selectable Core Voltage](#) **Free**

Duration: 45 minutes

View Now, On Demand!

Today's systems demand more performance, higher integration, and customization capabilities, but most systems cannot afford an increase in power budget. Learn how Altera developed a high-end 65-nm FPGA that met system designers' needs to increase performance and logic capacity, while reducing power. You'll learn to use Altera's unique technologies that help reduce power consumption in your designs.

This net seminar includes an in-depth look at some of Altera's power optimization tools and techniques such as: Programmable Power Technology, selectable core voltage, power-optimized memory interfaces, and the Quartus® II software's PowerPlay power optimization.

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"Altera's performance improvements have been amazing as discussed in the [Using Stratix III FPGAs to Achieve Higher Performance Systems with Lower Power](#), net seminar. We are hoping to incorporate these features in future products."

- Scott Olsen,
Engineer, TechniScan,
Inc.





[How Fast is the Fastest FPGA? Stratix III Performance Capabilities](#) **Free**

Duration: 60 minutes
View Now, On Demand!

Knowing your FPGA's performance is important in understanding the performance of your system. Stratix® III FPGAs deliver the industry's highest performance with a 600-MHz core and high-performance DSP and internal memory capabilities. Stratix III FPGAs include optimized I/O interfaces with LVDS at 1.25 Gbps, and they are the only FPGAs that support DDR3 SDRAM memory interfaces at 400 MHz.

This net seminar discusses the 65-nm Stratix III fabric and I/O architecture, as well as the award-winning Quartus® II software. Additionally, this net seminar highlights the relative performance of the competing Virtex-5 FPGAs and the benchmarking methodology used in obtaining these results.

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[Implement PCIe, GbE & SRIO with Altera's New Low-Cost FPGAs](#) **Free**

Duration: 15 minutes
View Now, On Demand!

This 15-minute QuickCast provides an overview of Arria™ GX devices, Altera's newest FPGA family with transceivers. Arria GX FPGAs support three protocols: PCI Express, Gigabit Ethernet, and Serial RapidIO, giving you a low-cost coprocessing option for your next design.

The net seminar discusses how Arria GX FPGAs' signal integrity, support and software compare to other offerings in the industry and how Altera's success with transceivers translates to a low-risk design for you. You'll hit the ground running and get your design out the door fast when you choose Arria GX FPGAs!

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Congratulations





Jon Childers of AT Systems Inc. was presented with a **Toshiba Gigabeat Portable Media Player** (US\$350) who won simply by attending Altera's net seminar, [Enable High-Volume Applications with New Low-Cost FPGAs.](#)

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