



## [Discover How Custom Logic Delivers Scalable Automotive Designs](#), 15 minutes

15 minutes

In this webcast, you'll see how using programmable logic and HardCopy<sup>®</sup> ASICs allows you to adapt quickly to changing automotive electronics requirements more effectively than using standard cell-based ASICs or ASSPs. You'll also learn about Altera's graphics and audio controller reference designs and automotive dev kits to jump-start your next design.



## [Comparing Stratix III and Virtex-5 Core Power](#), 4 minutes

Altera's proprietary Programmable Power Technology enables the Stratix<sup>®</sup> III logic fabric to consume less core power than Virtex-5 without compromising performance. See a side-by-side comparison in this **4-minute video!**

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## [Build 96-Port SGMII GigE with Stratix III FPGAs](#), 5 minutes

Stratix III FPGAs support SGMII GigE operation on LVDS I/O pins at 1.25 Gbps. With Stratix III FPGAs, you can build communications systems requiring single or multiple (up to 96) Ethernet links quickly and simply while meeting jitter and tolerance requirements. Watch it work in this **5-minute video!**

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## [Interfacing 1,067-Mbps DDR3 Memories to FPGAs](#), 5 minutes

Stratix III FPGAs, with their proprietary built-in I/O circuitry, reduce the challenges of high-speed DDR3 memory design. Watch this **5-minute video** to see how easy it is to interface 1,067 Mbps DDR3 memories to Stratix III FPGAs.

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[Accelerating OFDMA-MIMO Wireless System Designs](#), 30 minutes  
See how designers of future wireless technologies can leverage OFDMA-MIMO solutions from Altera to accelerate their system design. This 30-minute webcast provides an overview of channel card and RF card IP, reference designs, and development boards useful for WiMAX and LTE basestation development.

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