



[Manage Performance & Power Using 40-nm FPGAs](#), 30 minutes

In this 30-minute webcast, get a behind-the-scenes look at new technologies such as strained silicon and triple-gate oxide, new transceiver technology for the highest system bandwidth, and power optimization via the FPGA architecture and design software.

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[Building High-Speed Memory FPGA Interfaces](#), 15 minutes

View this 15-minute webcast to explore new 40-nm FPGA architectures built for reliable high-speed memory interface operation. You'll learn how to use IP to quickly and easily build memory interfaces, and how soft IP buys back timing margin by reducing timing variations.

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[Understand & Control Cost Drivers of Military FPGA Designs](#),

20 minutes

Designers of military electronics are faced with unique technical and cost design risks. This webcast looks at ways to mitigate these risks using high-end software tools, the impact of software productivity on defense program costs, and an efficient software flow can save money.

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[New Gen2 PCI Express Hard IP for 40-nm Devices](#), 15 minutes

When implementing PCI Express Gen2 in a design, each option offers unique pros and cons. This webcast delves into the four main benefits of implementing PCIe on new 40-nm Stratix® IV FPGAs and HardCopy® IV ASICs, emphasizing the industry transition from Gen1 to Gen2.

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[Video: 40-nm FPGA and 8.5-Gbps Transceiver Demo](#), 4 minutes

Watch this video and see Altera's 40-nm FPGA demonstration showing 1.5-Gbps LVDS performance and transceiver operation at 8.5-Gbps with excellent signal integrity.

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